

9 at least one switch connected to said at least one reference
B 10 resistor to selectively couple said voltage to a plurality of input word
11 lines wherein the ROM device uses said plurality of data resistors to
12 interconnect said plurality of input word lines with a plurality of output
13 bit lines.

B2 1 19. (Twice Amended) A method to maintain a current through Read-Only
2 Memory (ROM) substantially constant as temperature changes,
3 comprising the steps of:
4 selecting a reference resistor wherein said ROM employs a plurality
5 of data resistors to provide electrical interconnections between a plurality
6 of input lines and output lines and a change in electrical conductive
7 properties of said reference resistor matches a change in electrical
8 conductive properties of said data resistors;
9 supplying a reference voltage to said input lines, said reference
10 voltage developed by supplying a constant current to said reference
11 resistor, wherein said reference voltage is responsive to a change in
12 temperature.

B3 1 23. (Amended) In a ROM device, a temperature compensation circuit to maintain
2 a current through a selected one of a plurality of data resistors
3 substantially constant comprising:
4 at least one voltage source producing a voltage that is responsive
5 to changes in temperature; and
6 at least one switch connected to said at least one voltage source to
7 selectively couple said voltage to a plurality of input word lines wherein the
8 ROM device uses said plurality of data resistors to interconnect said
9 plurality of input word lines with a plurality of output bit lines.

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- 1 32. (Amended) A method to maintain a current through Read-Only Memory
2 (ROM) substantially constant as temperature changes, comprising the steps of:
- B' 3 supplying a reference voltage that is responsive to changes in
4 temperature to a plurality of input lines, wherein said ROM employs a
5 plurality of data resistors to provide electrical interconnections between
6 said plurality of input lines and a plurality of output lines and said
7 reference voltage changes to maintain said current through said data
8 resistors substantially constant.
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